

Page 18, paragraph 2, change as follows:

Figs. 5(1) - 5(5), 5(A)(1) - 5(A)(2) show diagrammatically the imaging system according to a presently preferred embodiment. The entire operation of the imaging system is controlled by a control processor 140, shown in Fig. 5A. Control processor 140 receives input commands from input controls 142 through 167 and provides output control signals 170 through 191. Control processors 140 provides control data to a beamformer 200, and provides image control data to image generator 250, which includes processing and display electronics. Beamformer 200 includes a transmit beamformer 200A and a receive beamformer 200B, shown diagrammatically in Fig. 5B. In general, transmit beamformer 200A and receive beamformer 200B may be analog or digital beamformers as described, for example, in U.S. Patents 4,140,022; 5,469,851; or 5,345,426 all of which are incorporated by reference.

Page 19, paragraph beginning of line 31, change as follows:

Control processor 140 provides delay commands to transmit beamformer channels  $215_1, 215_2, \dots, 215_M$  via a bus  $216_1$ , and also provides delay commands to the intra-group transmit pre-processors  $210_1, 210_2, \dots, 210_M$  via a bus 211. The delay data steers and focuses the generated transmit beams over transmit scan lines of a selected transmit pattern, as shown for example in Figs. 6 through 6C. Control processor 140 also provides delay commands to receive beamformer channels  $225_1, 225_2, \dots, 225_N$  via a bus 226 and delay commands to the intra-group receive pre-processors  $220_1, 220_2, \dots, 220_N$

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